Lab 6 Descriptions

1. This program uses verilog code to implement an 8 bit adder, and display the result onto HEX[2:0]
2. This program uses verilog code to implement an 8 bit adder/subtracter, and display the result onto HEX[2:0]
3. This program uses verilog code to implement a four bit multiplier, and display the result onto HEX[2:0]
4. This program uses verilog code to implement a 10 bit multiplier, and display the result onto HEX[3:0]
5. This program uses verilog code to implement an array tree adder to implement a multiplier, and display the result onto HEX[3:0]